

## CENTRE DE FORMATION AGRÉÉ

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# FORMATION ALLEGRO Sigrity POWER DC AND OPTIMIZE PI Filière Placement & Routage Cadence (PCB Design Environment)

**Ref: ALL-SIDCPI**

### Course Description

Sigrity™ PowerDC™ and OptimizePI™ provides a coherent methodology for the analysis of power delivery networks in high-speed printed circuit boards (PCBs). Power-delivery network design includes voltage regulator modules, decoupling capacitors, and power/ground planes. In this course, you use the Sigrity Power Integrity Suite software to analyze a stable power distribution system to support high-speed circuit operation.

### Duration

1 day

### Prerequisites

You must have experience with or knowledge of the following: Printed Circuit Board Design.

### Learning Objectives

After completing this course, you will be able to:

- Set up a board database for analysis
- Perform static IR-drop analysis
- Perform an electrical and thermal co-simulation
- Graph the impedance of the power distribution network across a complete frequency bandwidth
- Assign capacitor models to an existing printed circuit board
- Create and edit capacitor models
- Optimize capacitor selection for performance and cost

### Course Agenda

- Introduction to Sigrity Power Integrity Suite
- Database Translation
- Single Board Electrical/Thermal Co-Simulation
- Power Plane Impedance Analysis
- Capacitors and Plane Analysis Simulation
- Capacitor Optimization
- PowerTree

### Audience

Analog Designers - Design Engineers - Electrical Engineers

*Le formateur dispense cette formation en anglais sur un des sites Européen de Cadence Design Systems Inc*